

MICROENGINE TO NETWORK PROCESSING ENGINE INTERWORKING
FOR NETWORK PROCESSORS
Muthu Venkatachalam et al.
INTEL-008PUS

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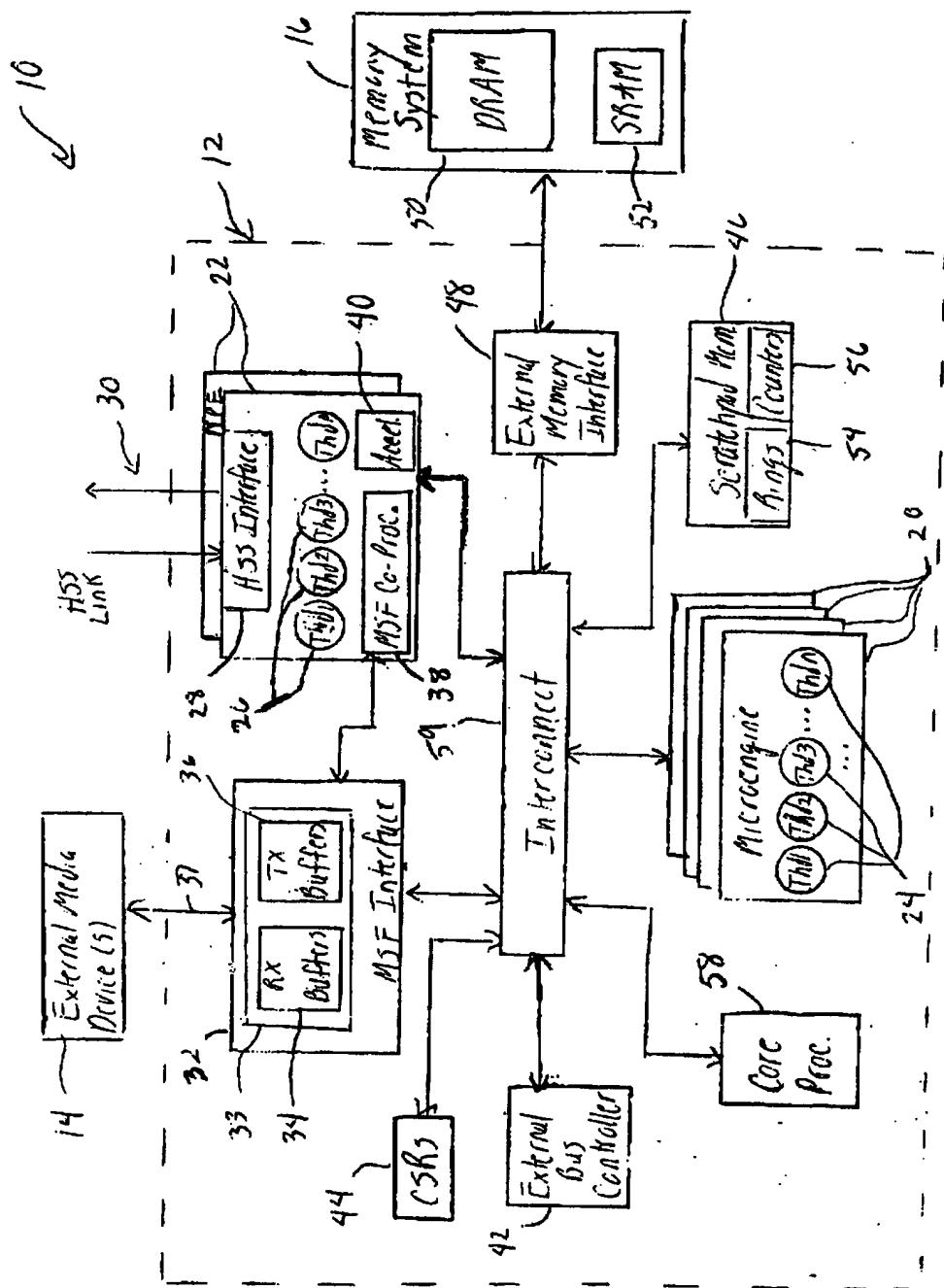


FIG. 1

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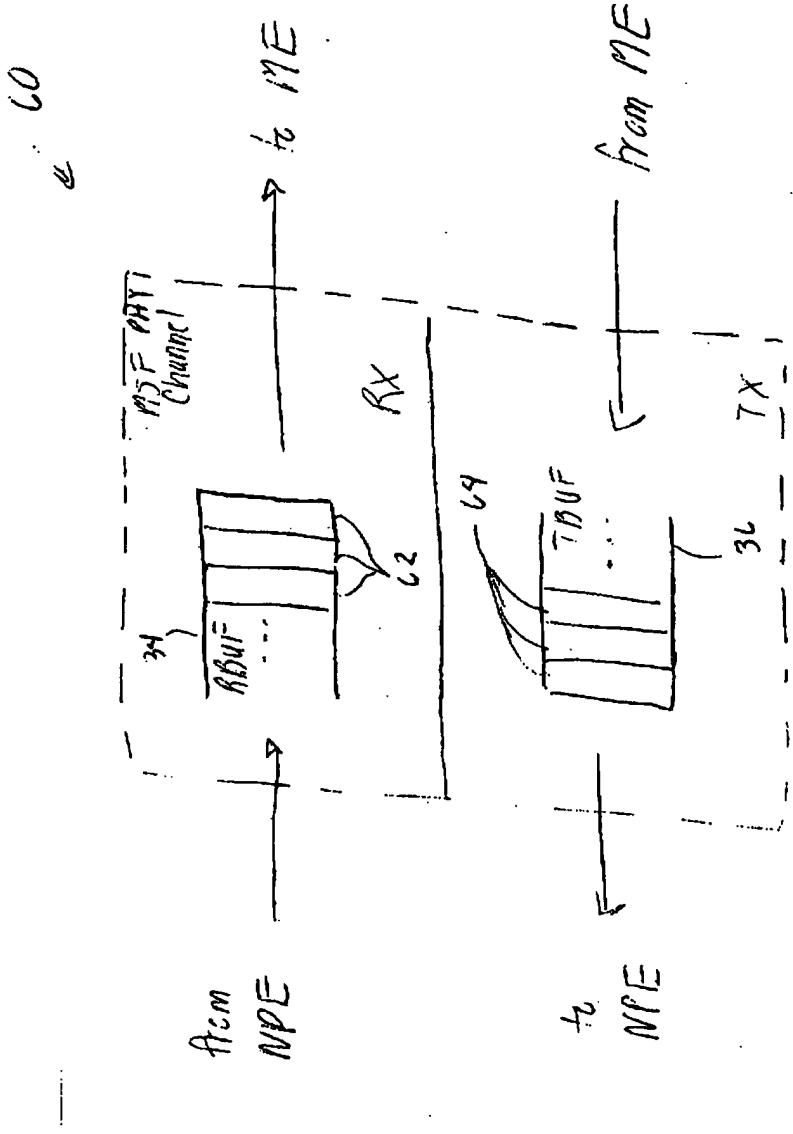


FIG. 2

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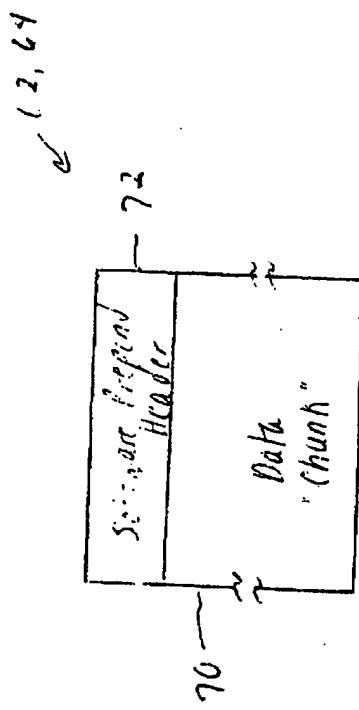


Fig. 3A

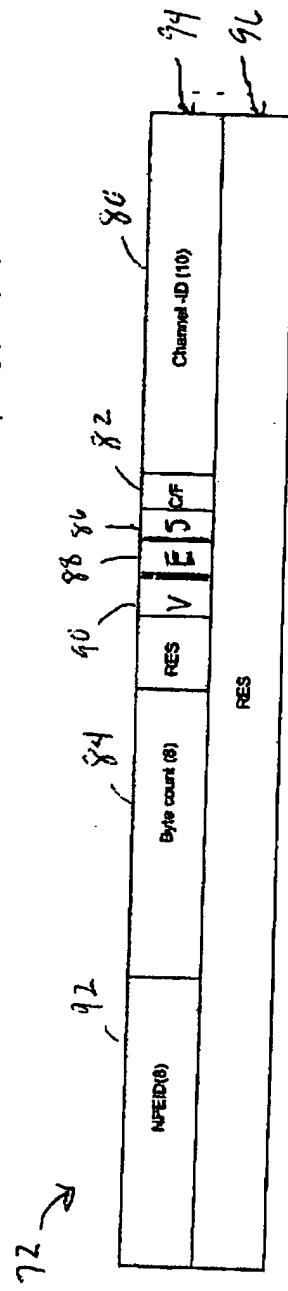


FIG. 3B

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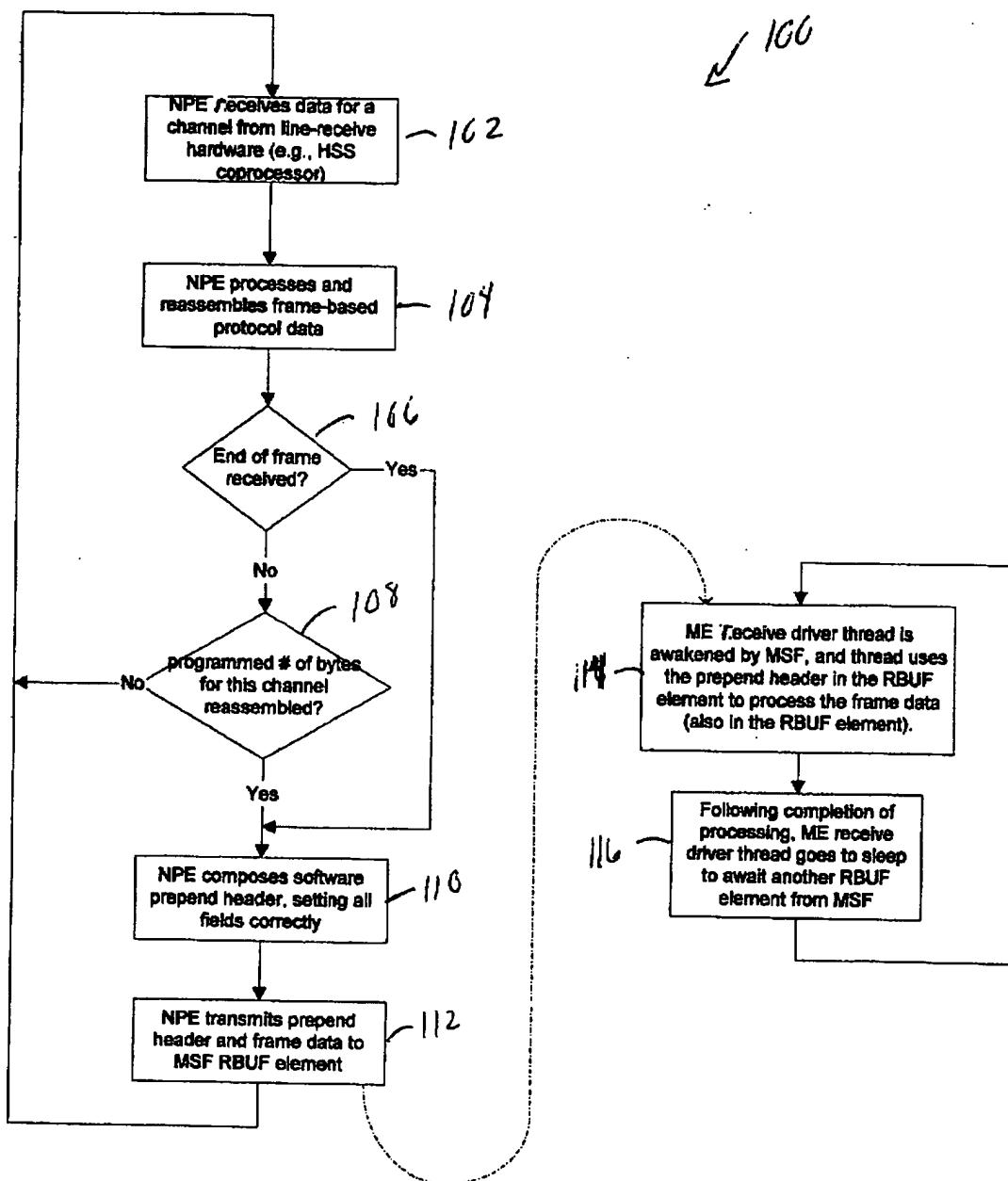


FIG. 4

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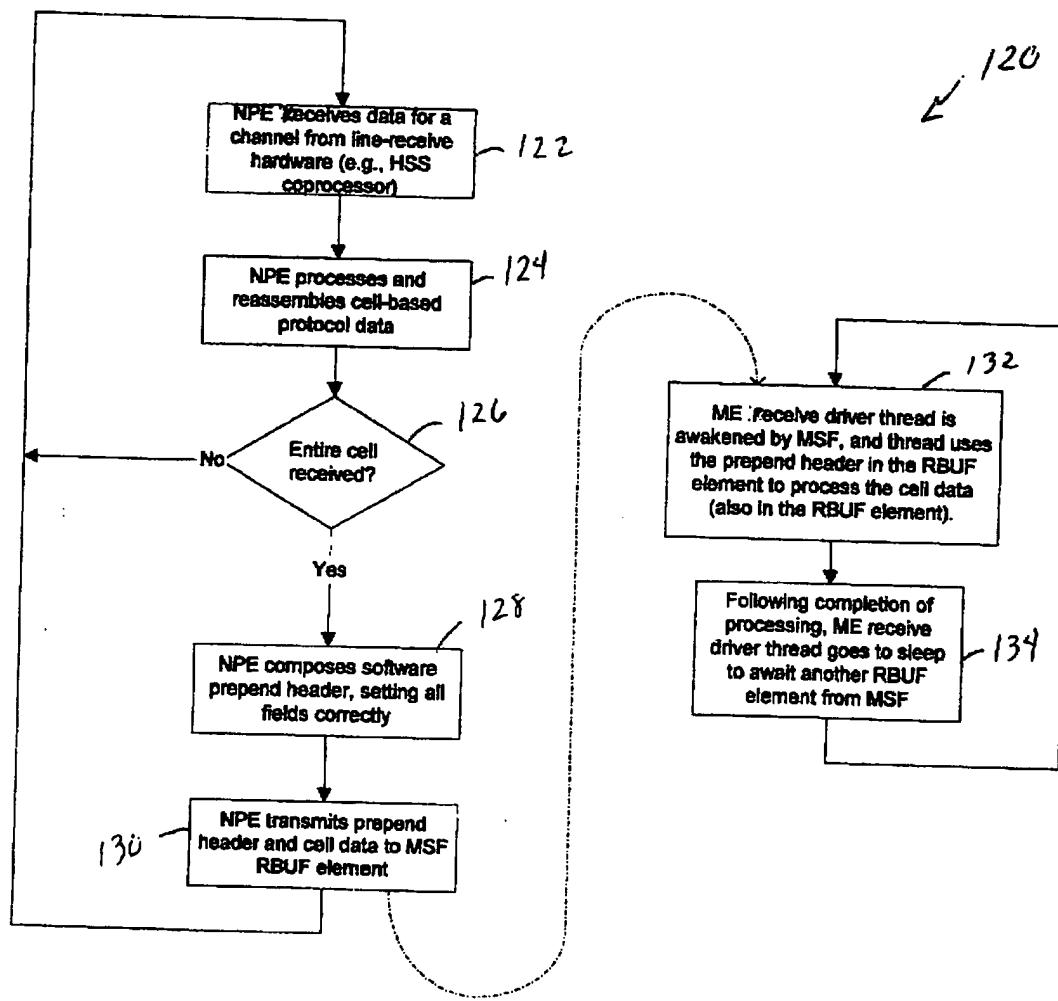


FIG. 5

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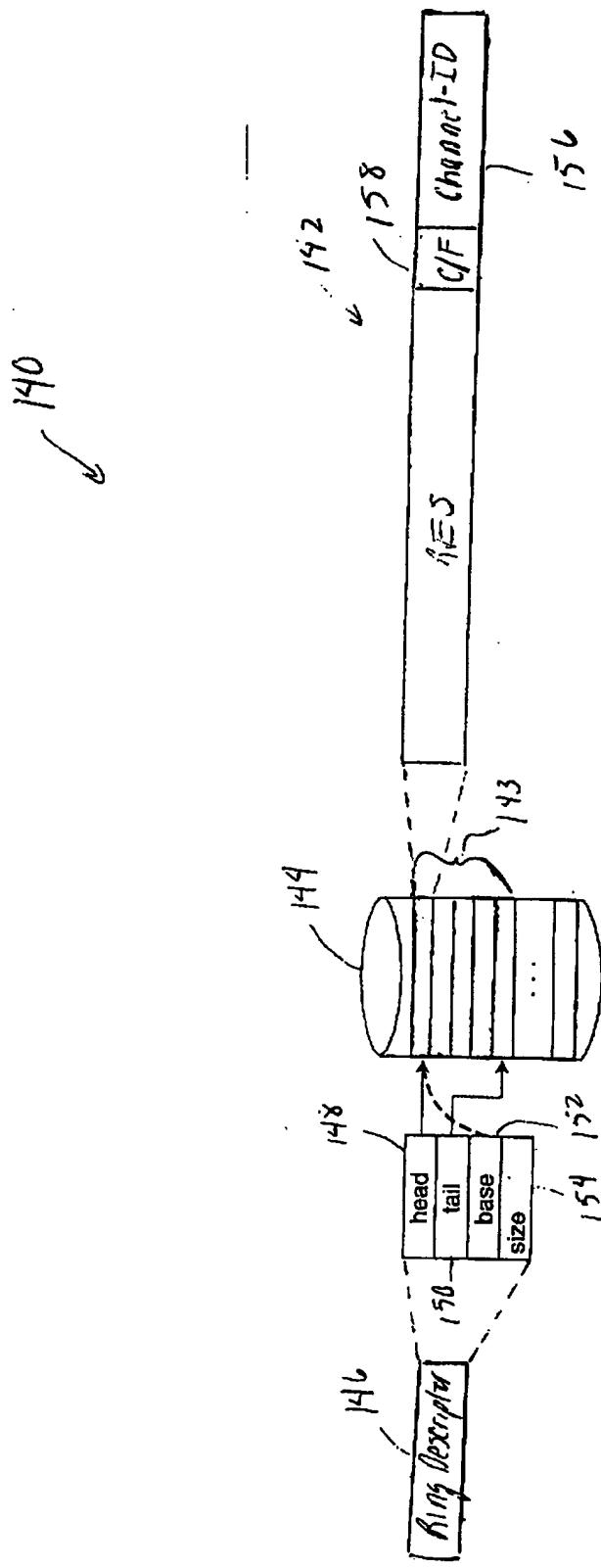


FIG. 6

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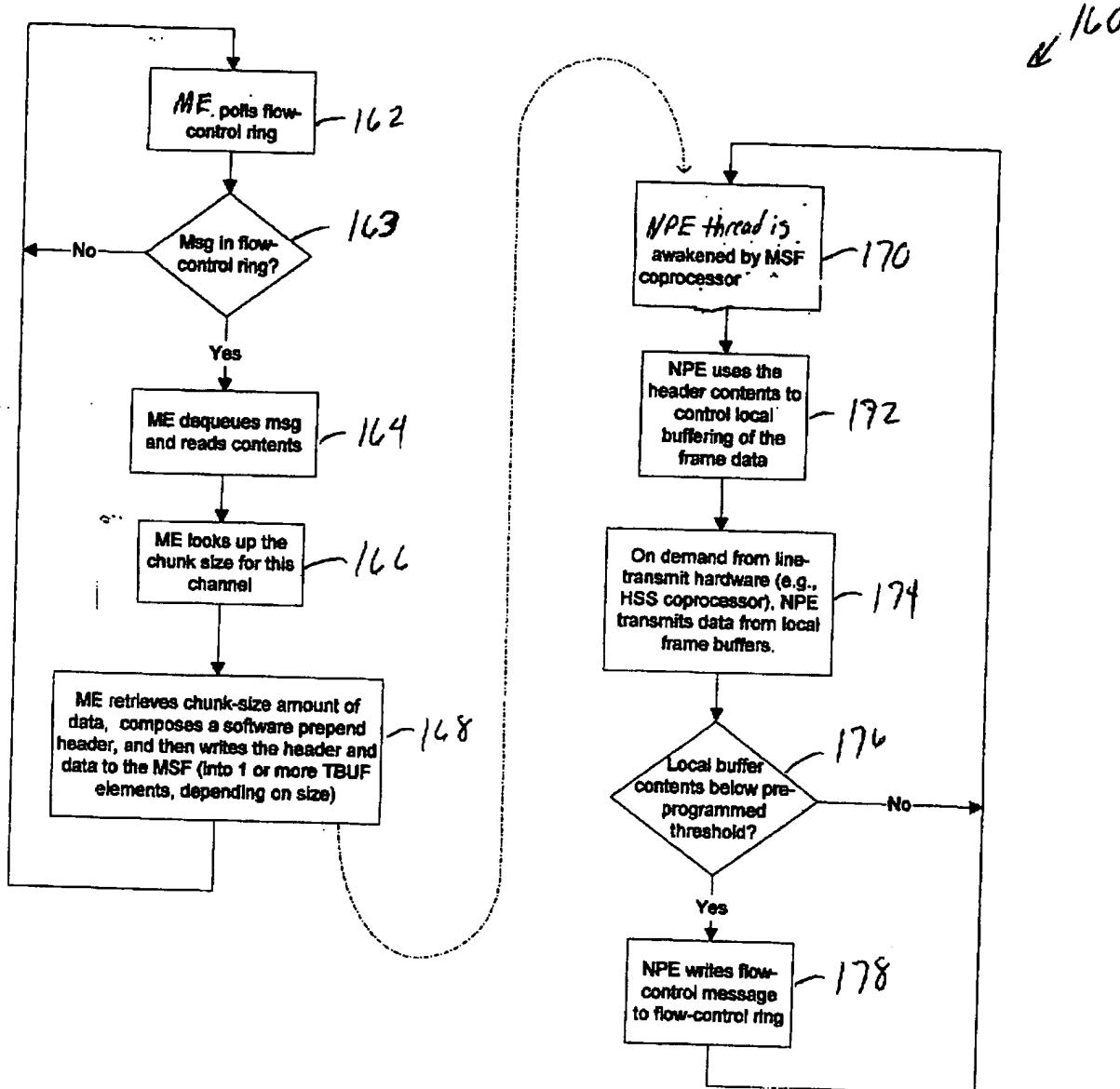


FIG. 7

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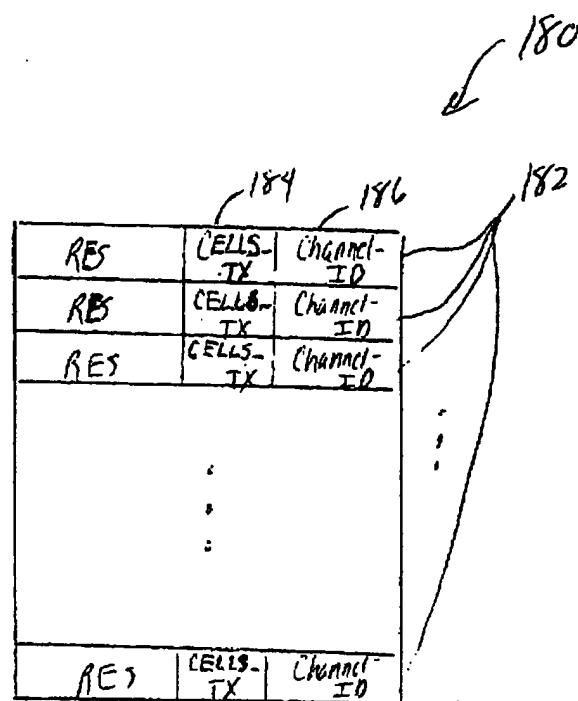


FIG. 8

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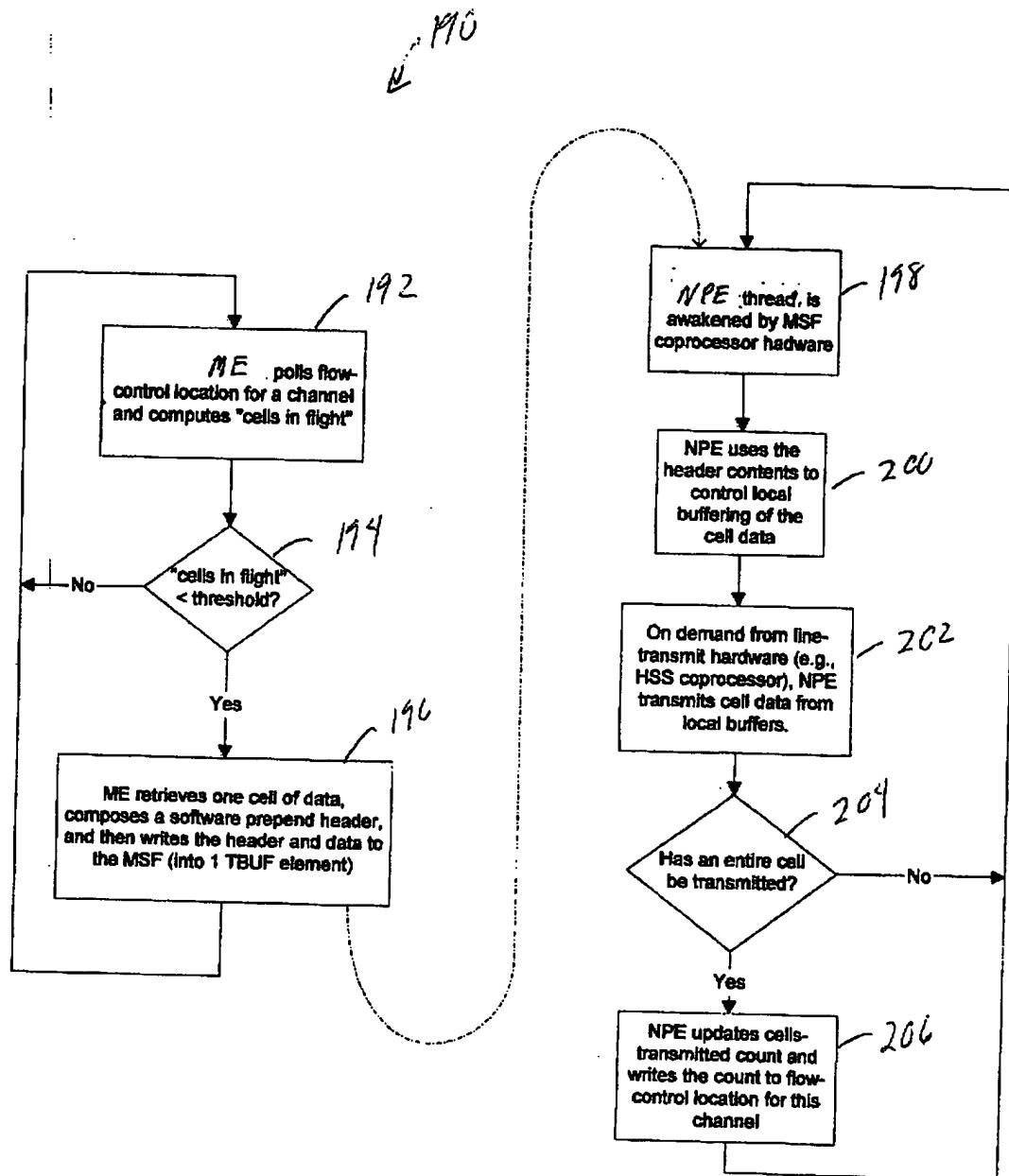


FIG. 9

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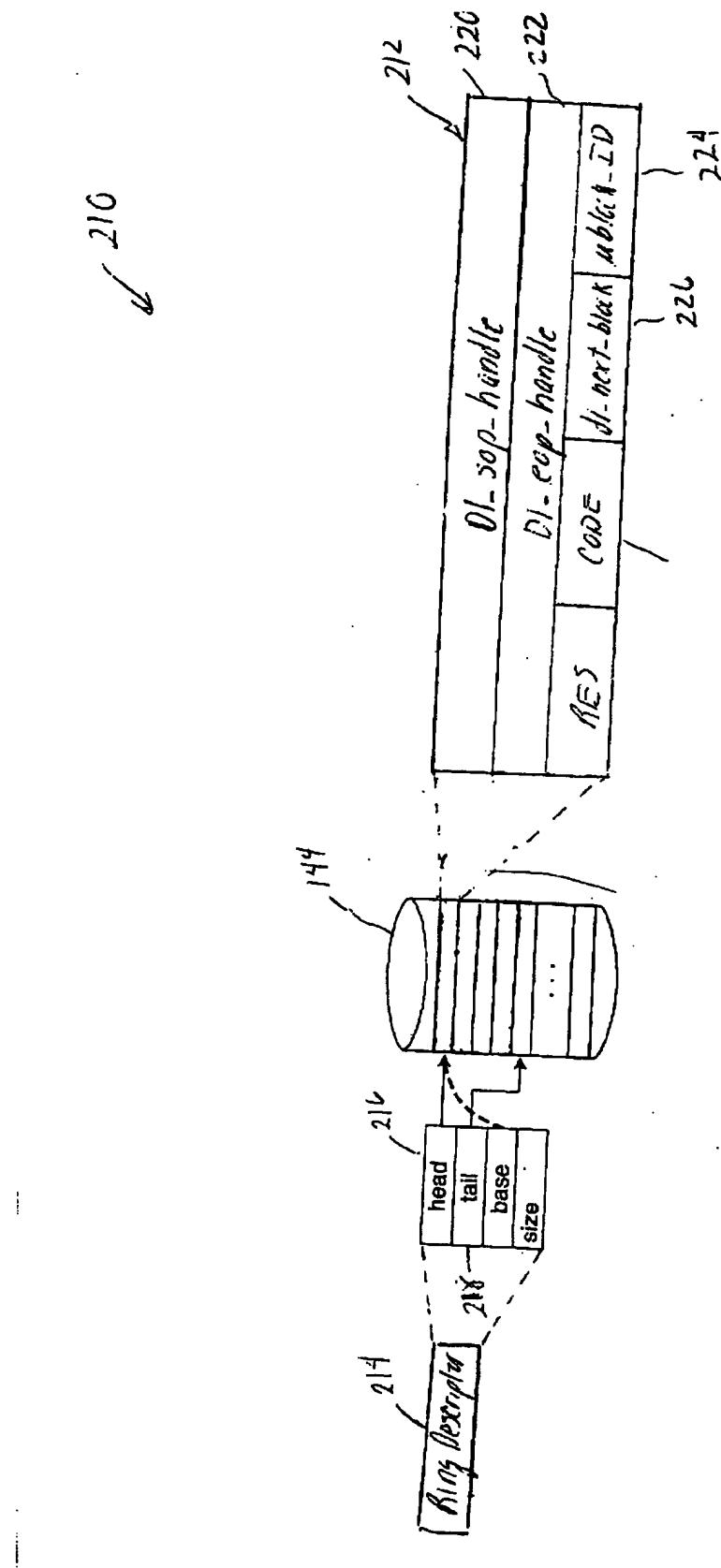


FIG. 10A

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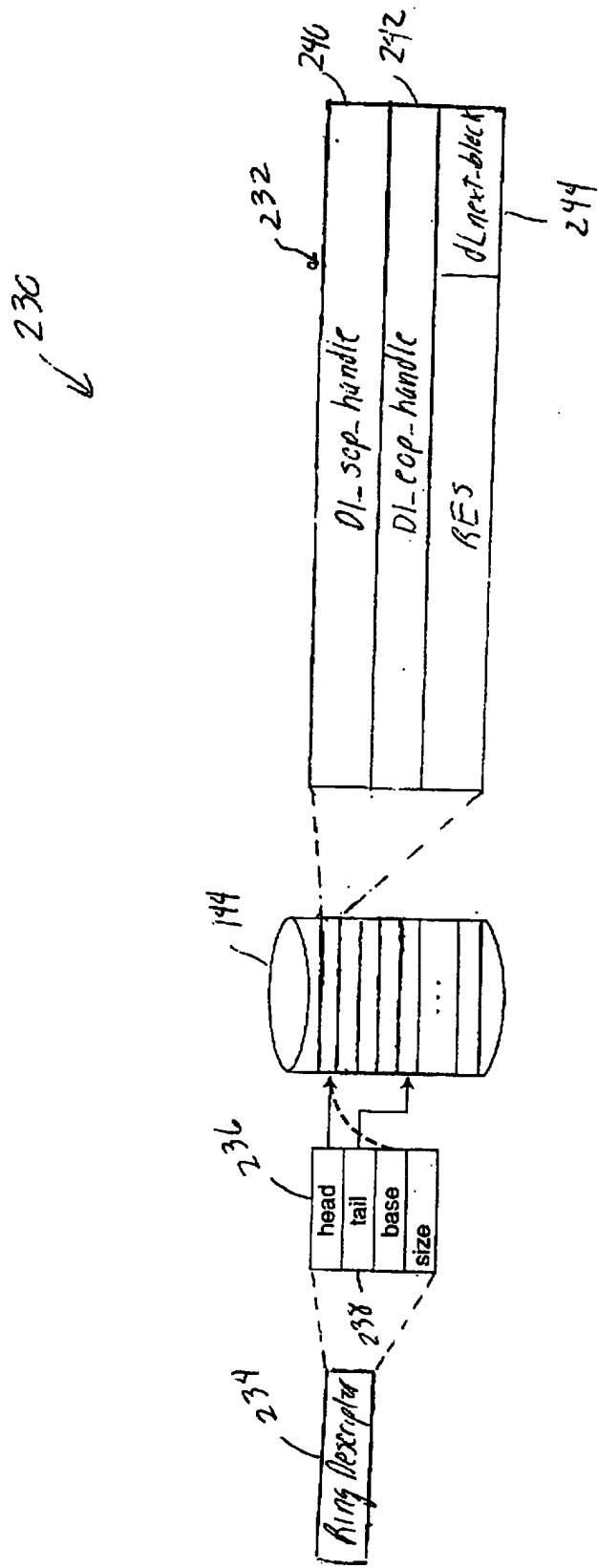


FIG. 10B

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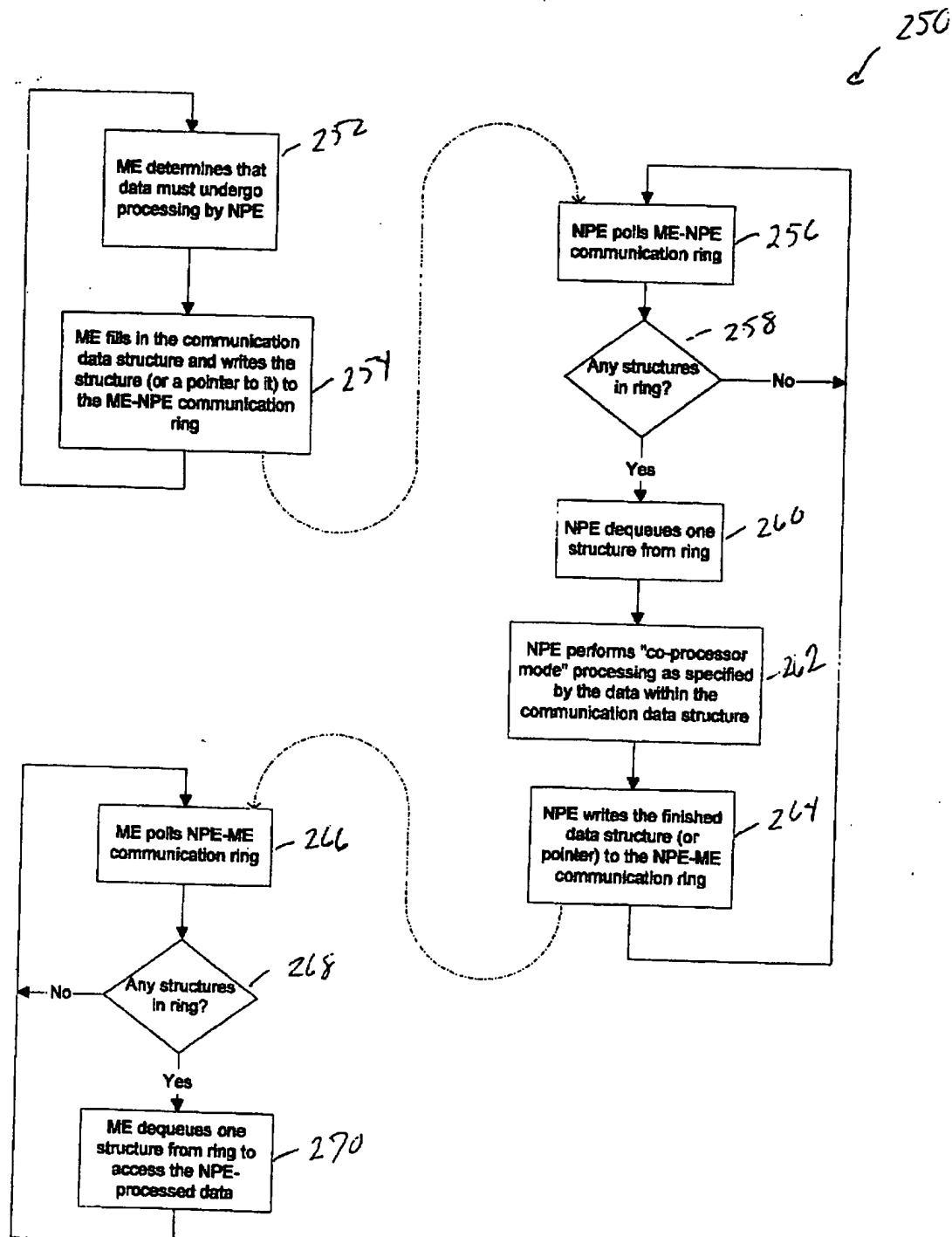


FIG. 11

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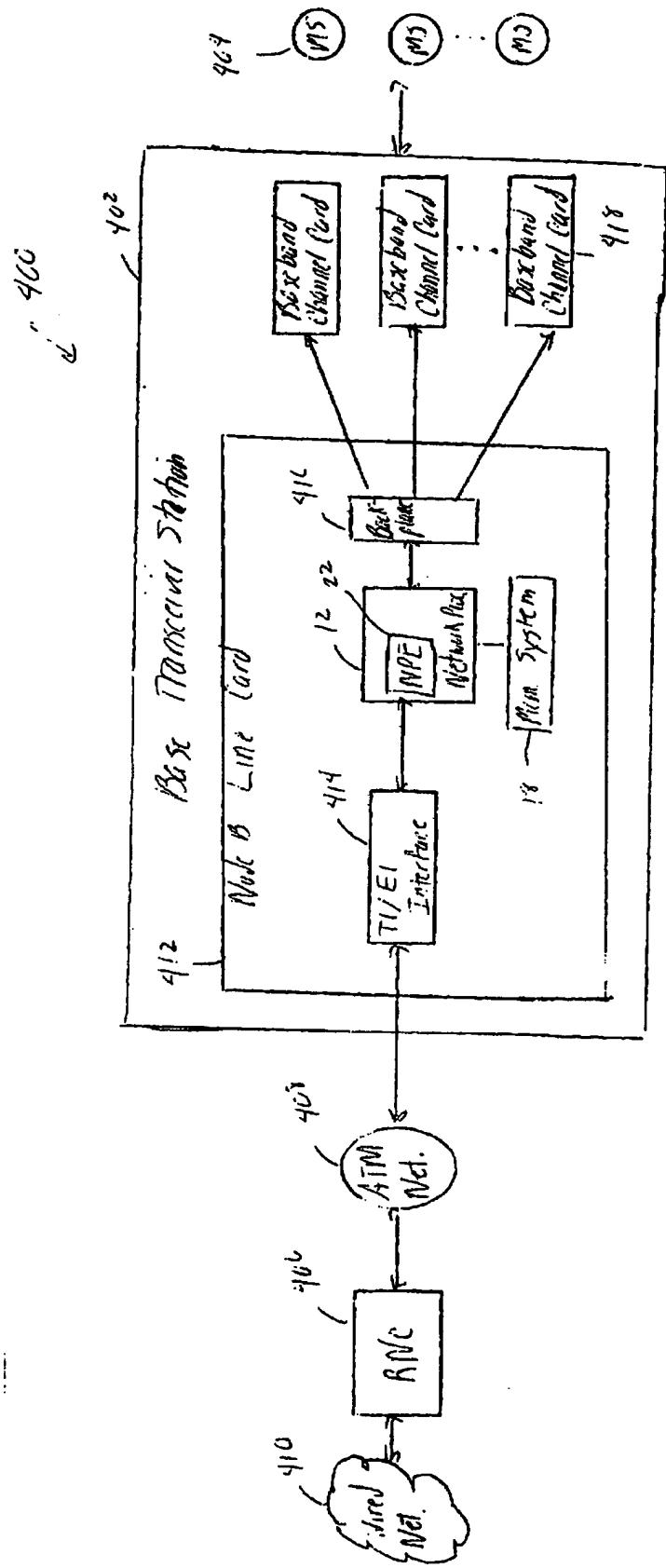


FIG. 12